



## **K3lsystemconfig - User's Manual**

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# Introduction

The **k3systemconfig** is a utility for creating and modifying the general parameters of operation of KHOMP boards, including the country setting of R2 signaling and interconnection between the bus boards CTBus.

The behavior of all types of boards KHOMP is identical with regard to CTBus settings, except for boards that have no connection with the bus CTBus.

## Parameters

The following parameters are accepted by the program:

- **--help**: Display program information, listing dispoíveis also the parameters for the command line.
- **--version**: Displays the program version.



# CTBus configuration

```

----- CTBus configurations -----
|
|           00 Operation frequency --->
|           01 Primary Master      --->
|           02 Synchronism         --->
|           03 CTbus connected board
|
|-----|
|
|           <Select>           <Return>           < Exit >
|
|-----|
    
```

- **Boards attached to the CTBus**

In this section this is the main option. Describes the relationship of boards connected on the bus CTBus. Card plugged into CTBus means that it is linked to another card or other cards via the bus CTBus.

The boards connected on CTBus can be programmed as primary master, secondary master or slave. A plaque attached to CTBus not mean she is working in isolation, ie not connecting to any other board through the bus.

All boards are not connected to CTBus automatically set as primary master. If a board connected to CTBus be programmed as not connected, will cause a conflict (contention) on the bus with the board as scheduled primary master, and one should check the physical connections carefully before performing the configuration.

## Frequencies of operation

```

----- Operation frequency -----
|
|           00 Serials: 00..03 (Mb/s)
|           01 Serials: 04..07 (MB/s)
|           02 Serials: 08..11 (Mb/s)
|           03 Serials 12..15 (Mb/s)
|
|-----|
|
|           <Select>           <Return>           < Exit >
|
|-----|
    
```

- **Serial: 00 .. 03 (Mb / s)**

Program the frequency of links 0-3.

- **Serial: 04 .. 07 (Mb / s)**

Program links the frequency of 8-11.

- **Serial: 08 .. 11 (Mb / s)**

Program the frequency of links 4-7.

- **Serial: 12 .. 15 (Mb / s)**

Program the frequency of links 12-15.

**Note:** Where there are cards in the system only KHOMP the frequency of links can be programmed to 8.192 Mb / s.

**Note:** The serial links 16-31 always work to 8.192 Mb / s.

## Primary Master

```

----- Primary Master -----
| |                               | |
| |           00 Clock generation for other busses ---> | |
| |           01 Clock gerado no CTbus                   | |
| |-----| |
| |
+-----+
| |                               | |
| |           <Select>           <Return>           < Exit > | |
| |-----| |
    
```

- **Clock generated in CTBus**

Selects which of the clock CTBus board primary master should generate. The card generates the secondary master clock unmarked.

## Clock generation for other buses

```

----- Clock generation for other busses -----
| |                               | |
| |           00 SCbus Frequency clock                   | |
| |           01 Generate H-MVIP clock                   | |
| |           02 Generate MVIP-90 clock                  | |
| |           03 Generate SCbus clock                    | |
| |-----| |
| |
+-----+
| |                               | |
| |           <Select>           <Return>           < Exit > | |
| |-----| |
    
```

The card may generate the bus clock rates compatible with the buses SCbus, VVIPs-90 and H-VVIPs, allowing connection with these other types of buses. Enable the board to generate these clocks **only when there is interconnection of these buses in CTBus**.

The bus clock SCbus can be configured for 2, 4 or 8MHZ.

00 SCbus clock frequency

H-01 Generate clock VVIPs  
Generate clock VVIPs 02-90  
03 Generate clock SCbus

## Synchronization

```
----- Synchronism -----
|-----|
| 00 Primary master board |
| 01 Network referece board (CT_NETREF) |
| 02 Secondary Master board |
| 03 Automatic fallback between primary master and secondary master |
| 04 CT_NETREF Frequency generated |
|-----|
+-----+
|           <Select>           <Return>           < Exit >           |
+-----+
```

- **Primary Master Card**

Select the card that will operate as primary master. When the card is programmed as not connected on the bus CTBus it will be scheduled as primary master.

- **Card reference network (CT\_NETREF)**

Select the card reference network. The reference network card is the card that generates the signal CT\_NETREF.

- **Secondary master card**

Select the card that will operate as a secondary master. If detected an error in the master clock generated by the primary, secondary teacher assumes the role of primary master only when the icon "Enable automatic fallback" is checked.

- **Automatic Fallback between the primary master and secondary master**

When this option is active, the promotion board to master primary secondary master in case of error in the primary master clock is run by the server KHOMP. When the application program is responsible for process this fallback option should not be activated.

- **Frequency Generation CT\_NETREF**

Determines the frequency of the signal CT\_NETREF. If a card is programmed to generate CT\_NETREF, it will generate with this frequency.